



IPW

Docket No.: M4065.0492/P492-A  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Leonard Forbes et al.

Application No.: 10/759,107

Confirmation No.: 6955

Filed: January 20, 2004

Art Unit: 2815

For: METHODS OF FORMING OUTPUT  
PREDICTION LOGIC CIRCUITS WITH  
ULTRA-THIN VERTICAL TRANSISTORS  
(AS AMENDED)

Examiner: J. R. Diaz

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated June 30, 2005 (Paper No. 06202005),  
please amend the above-identified U.S. patent application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins  
on page 3 of this paper.

**Remarks** begin on page 7 of this paper.